

SYNERGY SCHOOL OF ENGINEERING DEPARTMENT OF ELECTRICAL ENGINEERING

Discipline :Electrical Engineering	Semester:- 4 th sem	Name of the teaching faculty:- PRAJNAPARAMITA KABI
Subject:-	No. of	No. of weeks:-15
Analog electronics and	Days/week class	SESSION-2023-2024 SUMMER
op-amp	Allotted:-4	3E3310N-2023-2024 3UNIMER
No. of week	No. of class	Topic to be Taught
	Is	Introduction class
	2 nd	P-N Junction Diode Working of Diode
	3 rd	V-I characteristic of PN junction Diode.
l _a	4 th	De load line
	1 _{st}	Important terms such as Ideal Diode, Knee voltage
		Important terms such as Ideal Diode, Knee voltage . Junctions break
	2 nd	down. Zener breakdown Avalanche breakdown
	3 rd	P-N Diode clipping Circuit. P-N Diode clamping Circuit
2 nd	4 th	Thermistors,
	18	Sensors & barrettes
	2 nd	Zener Diode
	3 rd	Tunnel Diode
3 rd	4 th	PIN Diode
3	18	Classification of rectifiers
	2 nd	Analysis of half waverectifiers and calculate
4th	3 rd	full wave centre tapped and Bridge rectifiers and calculate:
4th	4 th	DC output current and voltage RMS output current and voltage Rectifier efficiency
5th	1 ^s	Ripple factor Regulation Transformer utilization factor Peak inverse voltage
	2 nd	Shunt capacitor filter Choke input filter
	3 rd	Choke input filter and π filter
	4 th	Principle of Bipolar junction transistor
6 th	l a	Principle of Bipolar junction transistor
	2 nd	Different modes of operation of transistor
	314	Current components in a transistor
	4 th	Transistor Transistor as an amplifer
	1 st	Transistor circuit configuration & its characteristics
	2 nd	CB Configuration CE Configuration CC Configuration
	3 rd	Transistor biasing
7th	4 th	Transistor biasing Stabilization

	14	Stability factor
	5161	Different method of Transistors Biasing
	314	Base resistor method
Sth	4m	Collector to base bias
	/a	Self bias or voltage divider method
	2114	transistor amplifier
	319	DC load line and DC equivalent circuit
9th	4th	AC load line and AC equivalent circuit
	la	Calculation of gain
	2 nd	Phase reversal
10th	310	H-parameters of transistors
Tout	4 th	Simplified H-parameters of transistors
11th	la	Generalised approximate model
	2nd	Analysis of CB, CE, CC amplifier using generalised approximate model
	3 rd	Multi stage transistor amplifier
	4 th	Feed back in amplifie
	l _a	Power amplifier and its classification
12th	2 nd	Oscillators Types of oscillators
	34	Classification of FET
	4 th	Advantages of FET over BJT
	la la	Principle of operation of BJT
13 th	2 nd	FET parameters (no mathematical derivation)
	3 rd	DC drain resistance
	4 th	AC drain resistance Trans-conductance
	I _a	General circuit simple of OP-AMP and IC – CA – 741 OP AMP
	2 nd	Operational amplifier stages
14 th	3 rd	Equivalent circuit of operational amplifier
	4 th	pen loop OP-AMP configuration
	la	OPAMP with fed back
	2 nd	Inverting OP-AMP
15 th	314	Non inverting OP-AMP Voltage follower & buffer
	4 th	Differential amplifier Adder or summing amplifie
	5 th	Integrator Differentiator Comparator

Signature of the faculty

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