## SYNERGY SCHOOL OF ENGINEERING, DHENKANAL

## LESSON PLAN Session (2023-2024)

Discipline: Computer Science & Engineering		Name of the faculty: Jayanta Kumar Behera
Subject: Computer System Architecture, (Th-1)	No. of Days/week: 04	Email: jayantabhr75@gmail.com Start Date: 02/08/2023 End Date: 04/12/2023

Week Class Day		Theory Topics	
1 <sup>st</sup>	1 <sup>st</sup>	1. Introduction to Basic structure of computer hardware	
	$2^{nd}$	Introduction of computer	
	3 <sup>rd</sup>	Basic Structure of computer	
	4 <sup>th</sup>	computer hardware	
2 <sup>nd</sup>	1 <sup>st</sup>	Functional Units	
	2 <sup>nd</sup>	Computer components	
	3 <sup>rd</sup>	Performance measures	
	4 <sup>th</sup>	Memory addressing & Operations	
3 <sup>rd</sup>	1 st	Revision	
	2 <sup>nd</sup>	2. Introduction to Instructions & instruction Sequencing	
	3 <sup>rd</sup>	Fundamentals to instructions	
	4 <sup>th</sup>	Operands	
4 <sup>th</sup>	1 <sup>st</sup>	Op Codes	
	2 <sup>nd</sup>	Instruction formats	
	3 <sup>rd</sup>	Addressing Modes	
	4 <sup>th</sup>	Continuing addressing modes	
5 <sup>th</sup>	1 <sup>st</sup>	Revision	
	2nd Sol	Question answer discussion	
	ionii (3rd) ii ya	3. Introduction to Processor System	
	1:4th Amaric	Register Files	
6 <sup>th</sup>	1 <sup>st</sup>	Complete instruction execution	
	2 <sup>nd</sup>	Hardware control	
·	3rd	Micro program control	
	4 <sup>th</sup>	Revision	
7 <sup>th</sup>	1 <sup>st</sup>	Quiz – 1	
	2 <sup>nd</sup>	4. Introduction to Memory System	
	3 <sup>rd</sup>	Memory characteristics	
	4 <sup>th</sup>	Memory hierarchy	
8 <sup>th</sup>	1 <sup>st</sup>	RAM and ROM organization	
	2 <sup>nd</sup>	Continuing about RAM and ROM organization	
	3 <sup>rd</sup>	Interleaved Memory	
	4 <sup>th</sup>	Cache memory	
9th	1 st	Virtual memory	

	2 <sup>nd</sup>	Revision	
	3 <sup>rd</sup>	Question answer discussion	
	4 <sup>th</sup>	5. Introduction to Input – Output System	
10 <sup>th</sup>	1 st	Input - Output Interface	
	2 <sup>nd</sup>	Modes of Data transfer	
	3rd	Programmed I/O Transfer	
	4 <sup>th</sup>	Interrupt driven I/O	
11 <sup>th</sup>	1 <sup>st</sup>	DMA	
	2 <sup>nd</sup>	I/O Processor	
	$3^{rd}$	Continuing I/O Processor	
	4 <sup>th</sup>	Revision	
12 <sup>th</sup>	1 <sup>st</sup>	Question answer discussion	
	$2^{nd}$	6. Introduction to I/O Interface & Bus architecture	
	$3^{rd}$	Bus and System Bus	
	4 <sup>th</sup>	Types of System Bus	
13 <sup>th</sup>	1 <sup>st</sup>	Bus Structure	
	$2^{nd}$	Basic Parameters of Bus design	
	3 <sup>rd</sup>	SCSI	
	4 <sup>th</sup>	USB	
14 <sup>th</sup>	1 <sup>st</sup>	Revision	
	$2^{nd}$	Quiz – 2	
	3 <sup>rd</sup>	7. Introduction to Parallel Processing	
	4 <sup>th</sup>	Parallel Processing	
. 15 <sup>th</sup>	1 <sup>st</sup>	Linear Pipeline	
	2 <sup>nd</sup>	Multiprocessor	
	3 <sup>rd</sup>	Flynn"s Classification	
	4 <sup>th</sup> .	Revision	

Prepared By:

HOD
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2r/07/23